

*Fig. 1*

200

OFFSET	NAME	LENGTH	VALUE	DESCRIPTION	
00h	Identifier	4 bytes	'FFCB'	Character string marks start of control block	210
04h	Present Firmware Family Code	1 word	Varies	Architecturally assigned family code which this image belongs to	220
06h	Present Firmware Stepping Level	1 byte	Varies	Stepping level automatically incremented by one for each new build	230
07h	First table entry	4 bytes	Varies	First compatibility table entry (optional)	240
08h	Second table entry	4 bytes	Varies	Second compatibility table entry (optional)	250
:	:	:	:	:	
03h + 4*N	Nth table entry	4 bytes	Varies	Nth compatibility table entry (optional)	260
07h + 4*N	End of table marker	4 bytes	00h 00h 00h 00h	Table entry of all 0's marks end of table	270

Fig. 2

300

OFFSET	NAME	LENGTH	VALUE	DESCRIPTION
00h	Relationship Code	1 byte	Varies	Code describing relationship between the present image and other images which may be compared to it
01h	Comparison Firmware Family Code	1 word	Varies (0001h or larger)	Firmware family code of another image which may be compared to the present image
03h	Comparison Firmware Stepping Level	1 byte	Varies (01h or larger)	Firmware stepping level of another image which may be compatible to the present image

310

320

330

Fig. 3

Method and System for Identifying Compatibility Between Firmware Images

Fig. 4

